

Electronic Acknowledgement Receipt

EFS ID:	3123086
Application Number:	10790907
International Application Number:	
Confirmation Number:	1415
Title of Invention:	INTEGRATED CIRCUIT WITH RE-ROUTE LAYER AND STACKED DIE ASSEMBLY
First Named Inventor/Applicant Name:	Jochen Thomas
Customer Number:	48154
Filer:	Ira Stuart Matsil/Kristin Hayes
Filer Authorized By:	Ira Stuart Matsil
Attorney Docket Number:	INF 2003 P 54322 US
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	03P54322.pdf	101577	no	1
			97dac40707e2c1a380a71ab6a8a3ee6e d0a1285c		

Warnings:

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2	Fee Worksheet (PTO-06)	fee-info.pdf	8324	no	2
			18b46030b818e487dd1a6f579df201e1 58ad243		

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